

Constructing $NP^{\#P}$ -complete problems and $\#P$ -hardness of circuit extraction in phase-free ZH calculus

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Overview

1. Background
2. Boolean formulae in ZH
3. Circuit extraction
4. $NP^{\#P}$ -complete problems
5. Summary

Background

ZH and computational complexity

Some problems arising in ZH calculus are believed to be hard.

- Given a phase-free ZH diagram, can we find an equivalent circuit?
- Given two diagrams, are they equal?

This talk:

- Circuit extraction is $\#P$ -hard
- Two problems related to comparing diagrams are $NP^{\#P}$ -complete

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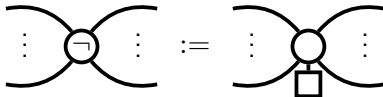
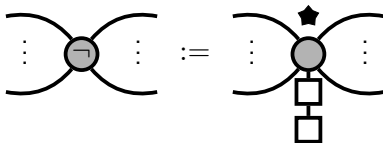
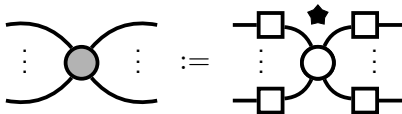
Phase-free ZH calculus

$$\left[\left[n \left\{ \begin{array}{c} \text{---} \\ \text{---} \\ \vdots \\ \text{---} \end{array} \right\} \bullet \left\{ \begin{array}{c} \text{---} \\ \text{---} \\ \vdots \\ \text{---} \end{array} \right\} m \right] \right] = \begin{pmatrix} 1 & & & \\ & 0 & & \\ & & \ddots & \\ & 0 & & 0 \\ & & & & 1 \end{pmatrix} \quad (2^m \times 2^n \text{ matrix})$$

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$$\left[\blacklozenge \right] = \frac{1}{2} \quad (\text{scalar})$$

Phase-free ZH calculus



Computational Complexity – NP

NP – problems solvable by a polynomial time **non**-deterministic Turing Machine (NDTM).

SAT

Input: *Variables x_1, \dots, x_n and a boolean formula ϕ on (some of) x_1, \dots, x_n*

Output: *True when ϕ is satisfiable, False otherwise.*

For example, $\text{SAT}((x_1 \wedge x_2) \wedge (x_1 \wedge \neg x_3)) = \text{True}$.

Computational Complexity – #P

#P – problems of the form: given a polynomial time NDTM and an input a , compute how many runs accept a .

#SAT

Input: Variables x_1, \dots, x_n and a boolean formula ϕ on (some of) x_1, \dots, x_n

Output: Number of satisfying assignments of ϕ

For example, $\#SAT((x_1 \wedge x_2) \wedge (x_1 \wedge \neg x_3)) = 1$.

Oracles

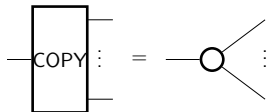
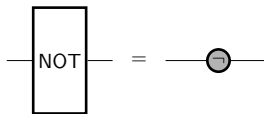
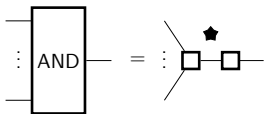
$\text{NP}^{\#\text{SAT}}$ – problems solvable by a polytime NDTM with access to oracle for $\#\text{SAT}$. By completeness, this class equals $\text{NP}^{\#\text{P}}$.
An oracle call counts as a single step of computation.

Boolean formulae in ZH

Logic in ZH

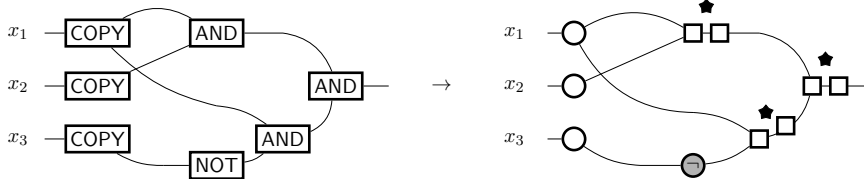
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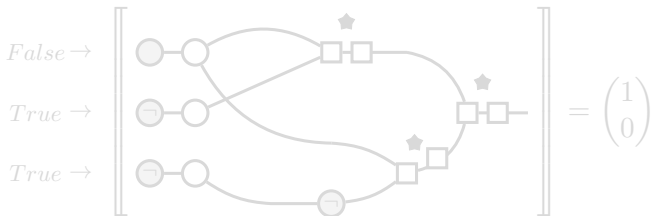


Boolean formulae in ZH

$(x_1 \wedge x_2) \wedge (x_1 \wedge \neg x_3)$ in ZH:

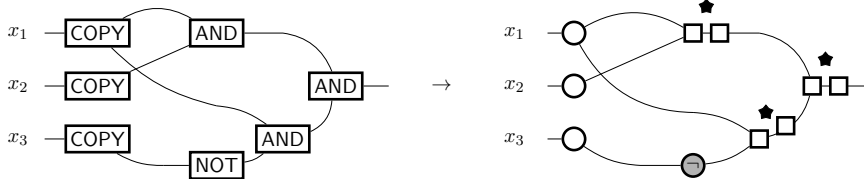


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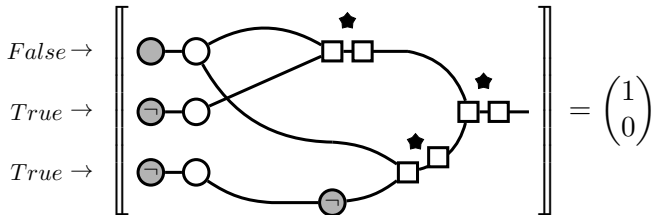


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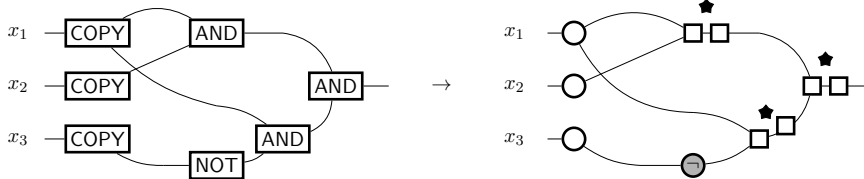


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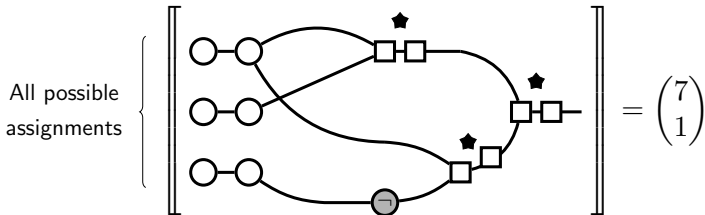


Boolean formulae in ZH

$(x_1 \wedge x_2) \wedge (x_1 \wedge \neg x_3)$ in ZH:



All assignments:



Circuit extraction

Circuit extraction hardness

Circuit Extraction

Input: A phase-free ZH diagram D proportional to a unitary and set of unitaries \mathcal{G} acting on $O(1)$ qubits.

Output: A polynomial (in size of D) circuit C , constructed from \mathcal{G} , expressing unitary proportional to D , or a message that no such circuit exists.

Theorem

Circuit Extraction is $\#P$ -hard.

Proof idea: reduce from $\#SAT$, i.e. show $\#SAT \in FP^{\text{Circuit Extraction}}$.

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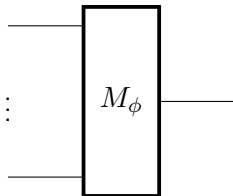
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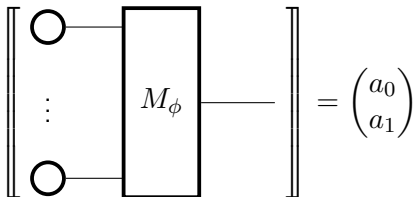
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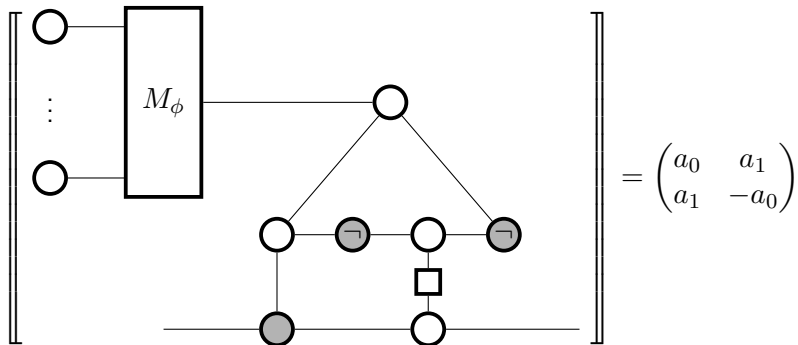
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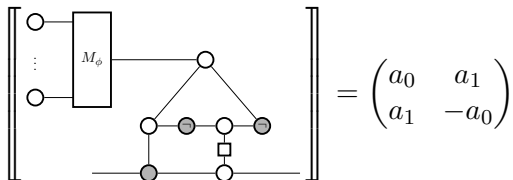


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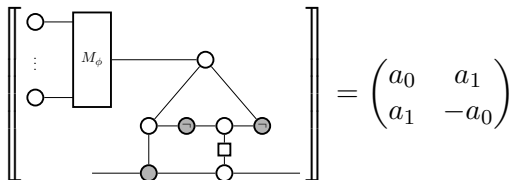


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- Suppose, we have a circuit C proportional to the above diagram
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$\text{NP}^{\#P}$ -complete problems

More dangling edges

- A diagram with k dangling edges has a matrix representation of the size 2^k .
- Given a diagram D , finding a matrix entry in $\llbracket D \rrbracket$ on some given position is $\#P$ -hard and within $FP^{\#P}$.
- Informally: given D , checking some property of all entries of $\llbracket D \rrbracket$ could be $NP^{\#P}$ -hard (or $coNP^{\#P}$ -hard).

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Input: *two diagrams D_1, D_2 with matching dangling edges.*

Output: *True if $\llbracket D_1 \rrbracket = \llbracket D_2 \rrbracket$ and False otherwise.*

Upper bound $\text{coNP}^{\#\text{P}}$ idea:

Non-deterministically choose a position in matrix representations of D_1 and D_2 . Using the oracle, compute entries on such position e_1 and e_2 .

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Input: *Two phase-free ZH diagrams D_1 and D_2 with n dangling edges each.*

Output: *True if there exists a state $|V\rangle$ in n qubits computational basis such that D_1 and D_2 applied to $|V\rangle$ result in the same scalar, and False otherwise.*

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Do matrix representations agree on **all** positions?

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$\text{NP}^{\#\text{P}}$ -hardness proof overview

- Idea: reduce any problem A in $\text{NP}^{\#\text{P}}$ to State Equality.
- Take polytime NDTM \mathcal{M} with $\#\text{SAT}$ oracle that recognizes A
- Express run of \mathcal{M} on an input a as a boolean formula with extra conditions checking oracle uses
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How does \mathcal{M} work?

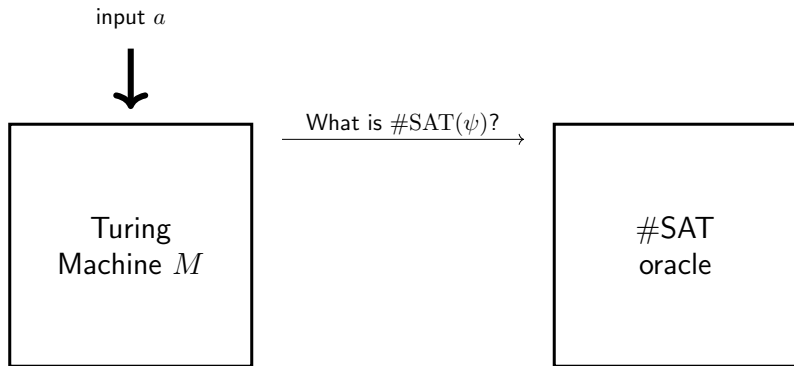
input a



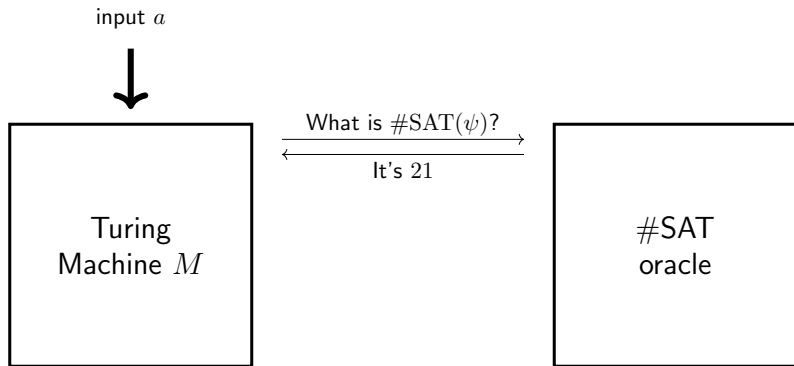
Turing
Machine M

#SAT
oracle

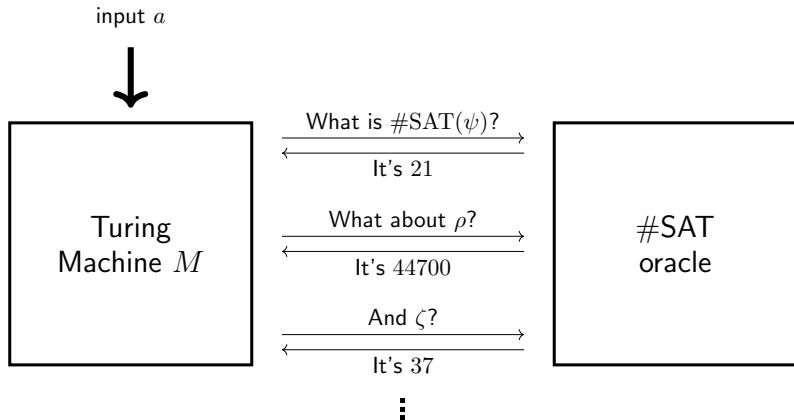
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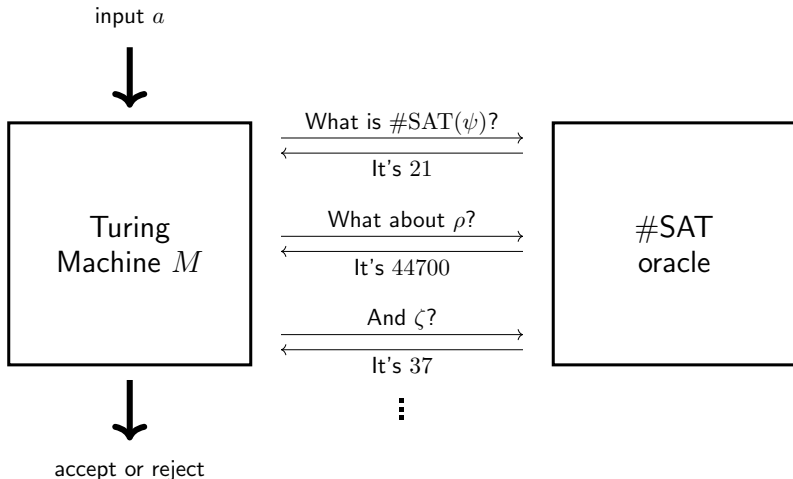
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Oracle calls

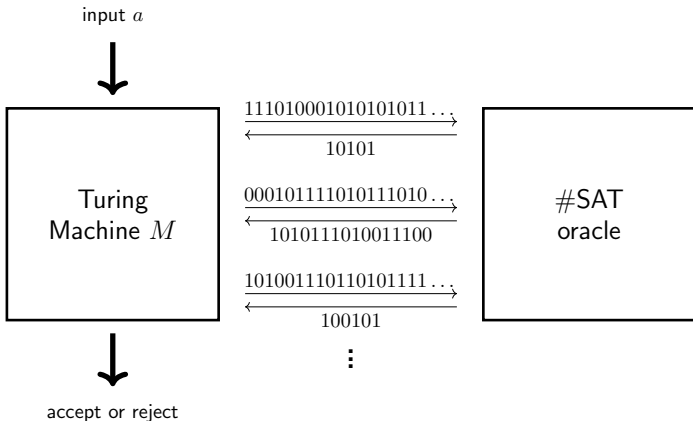
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- Same for head position, state etc.
- Combine into one formula ϕ_a , similar to the proof of the Cook-Levin theorem
- The meaning behind ϕ_a is as follows:
If ϕ_a is satisfied by some assignment V , then V encodes a path from initial configuration of \mathcal{M} on input a , to an accepting configuration, without checking that oracle returned correct data.

Oracle conditions

To verify oracle uses, we add conditions $C_{a,1}, C_{a,2} \dots$, where $C_{a,k}$ stands for:

If in k^{th} step an oracle is called on some input w , then in the $k + 1^{\text{th}}$ step, \mathcal{M} contains the result of running oracle on w .

Oracle conditions can be combined into a single condition C_a .

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Informal description

ϕ_a under valuation V means:

Does the run of \mathcal{M} on a given by V result in an accepting configuration, ignoring the oracle?

\mathcal{C}_a under valuation V means:

In run given by V , \mathcal{M} asked for $\#SAT(\psi), \#SAT(\rho), \dots$ and oracle returned ans . On its tapes, \mathcal{M} wrote number num . Does $ans = num$?

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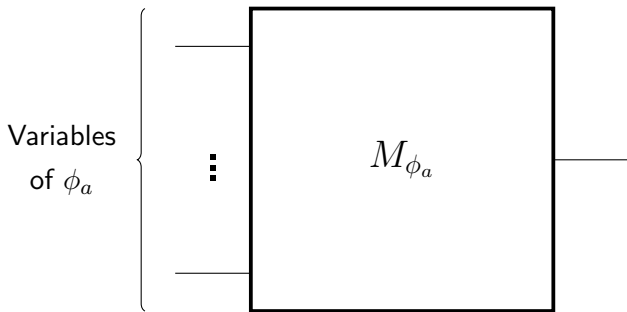
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- Given ϕ_a and \mathcal{C}_a we construct State Equality instance, i.e. two diagrams D_1 and D_2 .
- We already know how to encode ϕ_a .
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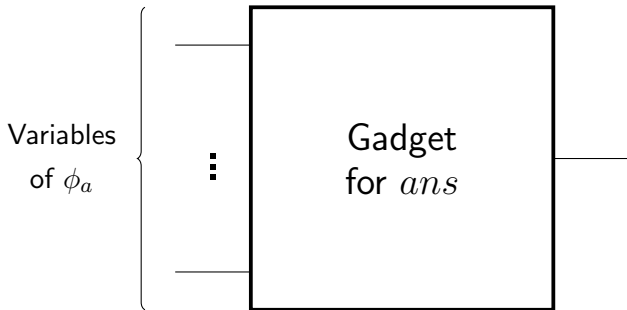
Formula ϕ_a



For $|V\rangle$ from computational basis:

$$\llbracket M_{\phi_a} \rrbracket |V\rangle = \begin{cases} |1\rangle, & \phi_a \text{ is satisfied under } V \\ |0\rangle, & \text{otherwise} \end{cases}$$

Oracle answers

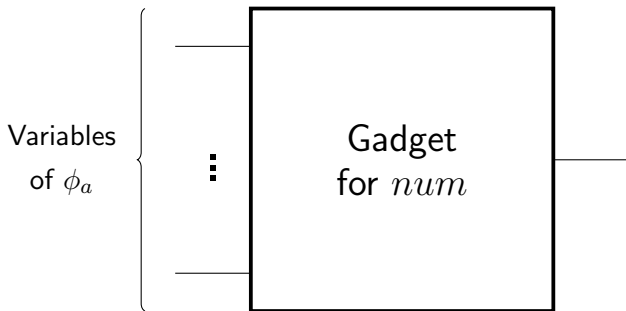


For $|V\rangle$ from computational basis:

$$\llbracket \text{Gadget for } ans \rrbracket |V\rangle = (2^P - ans) |0\rangle + ans |1\rangle$$

where ans is a concatenation of answers to the oracle queries.

Numbers written as oracle answers

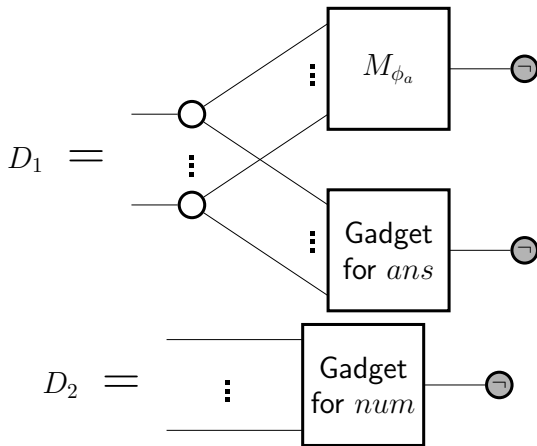


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Final constructions



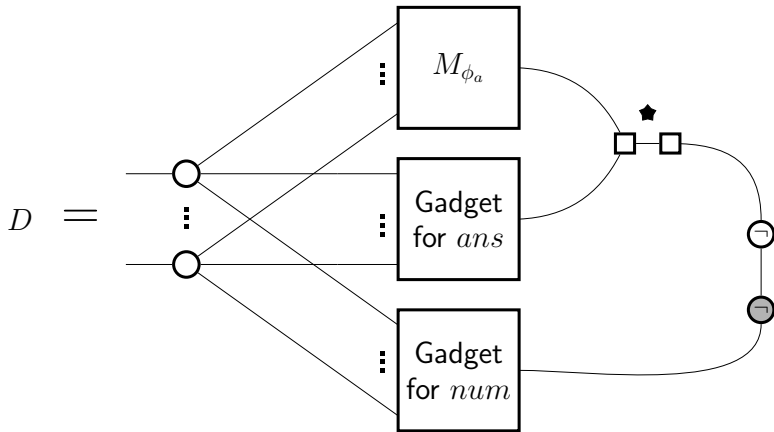
$$\llbracket D_1 \rrbracket |V\rangle = \begin{cases} 0, & \phi_a \text{ unsatisfied under } V \\ ans, & \text{otherwise} \end{cases} \quad \llbracket D_2 \rrbracket |V\rangle = num$$

Related problem

Matrix Entry

Input: A phase-free ZH diagram D with n dangling edges and a number $l \in \mathbb{Z}[\frac{1}{2}]$.

Output: *True* if matrix interpretation of D contains an entry equal to l , and *False* otherwise.



Summary

Summary and further work

- Connections of ZH and computational complexity
- Circuit Extraction is $\#P$ -hard
- State Equality and Matrix Entry are $NP^{\#P}$ -complete
- Can we improve circuit extraction bounds?
- Does the Turing Machine approach work for other problems?

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